

Welcome to KFUPM

Dr. Anees Ullah

Assistant Professor

Computer Engineering Department

Affiliated with Interdisciplinary Research Center for Intelligent Secure Systems



Education:

PhD. from the Polytechnic University of Turin, Italy, 2015

Previous Institution:

NWFP University of Engineering and Technology, Peshawar, Pakistan
Assistant Professor
2019-2025

Research Profile (SCOPUS):

Total Citations: **463**

Total Journal Publications: **33**

H-index: **15**

Professional Interests:

AI Hardware design, RISC-V SoC Design and Verification, Reconfigurable Computing, Fault Injection & Fault Tolerance, Radiation Hardening, In-NIC FPGA packet processing

About Dr. Anees Ullah:

Dr. Anees Ullah has over a decade of experience in teaching and research related to digital design and verification for both ASICs/FPGA technologies. His experience encompasses RISC-V SoC design and verification, fault tolerance and fault injection, Reconfigurable and Fault-tolerant Computing.